

(Help | Advanced search)

Go!

Search or Article-id

All papers 🚽

Download:

- PDF
- Other formats

Current browse context: cs.IT

< prev | next >

new | recent | 1107

Change to browse by:

References & Citations

NASA ADS

DBLP - CS Bibliography listing | bibtex A. Dinesh Kumar Ambedkar Dukkipati Bookmark(what is this?)



Computer Science > Information Theory

A Two Stage Selective Averaging LDPC Decoding

A. Dinesh Kumar, Ambedkar Dukkipati

(Submitted on 14 Jul 2011)

Low density parity-check (LDPC) codes are a class of linear block codes that are decoded by running belief propagation (BP) algorithm or loglikelihood ratio belief propagation (LLR-BP) over the factor graph of the code. One of the disadvantages of LDPC codes is the onset of an error floor at high values of signal to noise ratio caused by trapping sets. In this paper, we propose a two stage decoder to deal with different types of trapping sets. Oscillating trapping sets are taken care by the first stage of the decoder and the elementary trapping sets are handled by the second stage of the decoder. Simulation results on regular PEG (504,252,3,6) code shows that the proposed two stage decoder performs significantly better than the standard decoder.

Comments:5 pages, 4 figuresSubjects:Information Theory (cs.IT)Cite as:arXiv:1107.2867v1 [cs.IT]

Submission history

From: Dinesh Kumar A [view email] [v1] Thu, 14 Jul 2011 16:55:54 GMT (124kb,D)

Which authors of this paper are endorsers?

Link back to: arXiv, form interface, contact.