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Research Article

Diophantine Frequency Synthesizer Des Timekeeping Systems

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Abstract

Diophantine Frequency Synthesis (DFS), a number-theoretic approc synthesizers, was introduced in 2006. Further work concerning t spectral purity was addressed and reported at the 2007 European is on the implementation of nested DFS architectures targe timekeeping systems. We have shown that DFS does not impart purity in comparison to commonly used high resolution frequency Here we describe a design approach for 10 MHz synthesizers wi ranging ± 10 Hz. The synthesizers generate their output from a essential to accomplishing precision frequency correction in timeke