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A Method for the Design of Petri Net Controller Enforcing General Linear Constraints

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Abstract

The problem of constructing a Petri net feedback controller, which enforces linear inequality constraints involving the marking vector and the Parikh vector on a discrete event system (DES) modeled by Petri nets (PN), is discussed in this paper. A novel method for design of controller enforcing the constraints is presented. First the constraints involving the marking and Parikh vectors are transformed into the constraints involving Parikh vector only using Petri net state equality, and then the controller is constructed based on the viewpoint that a place can be seen as a linear inequality constraint on the Parikh vector. The method is proved to be simpler and more efficient than that presented by lordache and Moody through an applied instance that was also used by Moody et al., and holds remarkable advantage especially for large systems.

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摘要

针对基于Petri网离散事件系统关于标识向量和Parikh向量的不等式约束反馈控制器设计问题,提出一种新的控制器设计方法.该方法首先利用 Petri网的状态方程把关于标识向量和Parikh向量的不等式约束转变成关于Parikh向量的不等式约束,然后基于Petri网库所是关于Parikh向量的不

等式约束的观点构造控制器.最后将该方法与Iordache和Moody提出的方法作比较,实验结果显示该方法更简单、有效.

References:

[1] Murata T. Petri nets: Properties, analysis and applications. Proc. of the IEEE, 1989,77(4):541-580.

[2] Giua A, DiCesare F, Silva M. Generalized mutual exclusion constraints on nets with uncontrollable transitions. In: Proc. of the IEEE Int'l Conf. on SMC. 1992. 975-979. <u>http://intl.ieeexplore.ieee.org/Xplore/DynWel.jsp</u>

 [3] Yamalidou K, Moody JO, Lemmon M, Antsaklis PJ. Feedback control of Petri nets based on place invariants. Automatica, 1996,32(1):15-18.

[4] Moody J, Antsaklis PJ. Petri net supervisors for DES with uncontrollable and unobservable transitions. IEEE T-AC, 2000,45(3): 462-472.

[5] Holloway LE, Krogh BH. Synthesis of feedback logic for a class of controlled Petri nets. IEEE T-AC, 1990,35(5):514-523.

[6] Holloway LE, Guan X, Zhang L. A generalization of state avoidance policies for controlled Petri nets. IEEE T-AC, 1996,41(6): 804-816.

[7] Iordache UV, Moody JO, Antsaklis PJ. Synthesis of deadlock preventions supervisors using Petri nets. IEEE T-AC, 2002,18(1): 59-68.

[8] Iordache UV, Antsaklis PJ. Design of T-liveness enforcing supervisors in Petri nets. IEEE T-AC, 2003,48(11):1962-1974.

[9] Iordache UV, Antsaklis PJ. Synthesis of supervisors enforcing general linear constraints in Petri nets. IEEE T-AC, 2003,48(11): 2036-2039.

[10] Krogh BH, Holloway LE. Synthesis of feedback logic for discrete manufacturing systems. Automatica, 1991,27(4):641-651.

[11] Li Y, Wonham WU. Control of vector discrete-event systems II-The base model. IEEE T-AC, 1994,39(3):512-531.