



Engineering Village



航空学报 » 2001, Vol. 22 » Issue (3) :212-216 DOI:

论文

[最新目录](#) | [下期目录](#) | [过刊浏览](#) | [高级检索](#)

[<< Previous Articles](#) | [Next Articles >>](#)

## 高速矢量处理器的设计与实现

王俊,毛士艺,刘祥林

北京航空航天大学电子工程系 北京 100083

DESIGN AND ACCOMPLISHMENT IN A HIGH SPEED VECTOR PROCESSOR

WANG Jun, MAO Shi-yi, LIU Xiang-lin

Department of Electronic Engineering, Beijing University of Aeronautics and Astronautics, Beijing 100083, China

摘要

参考文献

相关文章

Download: [PDF \(185KB\)](#) [HTML \(OKB\)](#) Export: BibTeX or EndNote (RIS) [Supporting Info](#)

**摘要** 为了满足海量实时处理需求,利用 Butterfly DSP公司的 BDSP9124 /9320矢量数字信号处理 (Digital Signal Processing,DSP)芯片组,设计和实现了一种高速矢量处理器,并给出了该矢量处理器的设计思想和性能指标。最后说明该处理器在合成孔径雷达 (Synthetic Aperture Radar,SAR)脉冲压缩和其它领域的应用。

**关键词:** 实时 矢量处理 DSP FFT SAR

**Abstract:** This paper studied and accomplished a high speed vector processor. The main component of the processor is a special Digital Signal Processing (DSP) chip set BDSP9124/9320, which is produced by Butterfly DSP Inc. The processor could be applied to Synthetic Aperture Radar (SAR) and other fields which require real-time calculating FFT, correlation, convolution, and other vector processing.

**Keywords:** real-time vector processing DSP FFT SAR

Received 2000-04-17; published 2001-06-25

### 引用本文:

王俊;毛士艺;刘祥林. 高速矢量处理器的设计与实现[J]. 航空学报, 2001, 22(3): 212-216.

WANG Jun;MAO Shi-yi;LIU Xiang-lin. DESIGN AND ACCOMPLISHMENT IN A HIGH SPEED VECTOR PROCESSOR[J]. Acta Aeronautica et Astronautica Sinica, 2001, 22(3): 212-216.

### Service

- ▶ 把本文推荐给朋友
- ▶ 加入我的书架
- ▶ 加入引用管理器
- ▶ Email Alert
- ▶ RSS

### 作者相关文章

- ▶ 王俊
- ▶ 毛士艺
- ▶ 刘祥林