

A NOVEL IDEAL FLOATING INDUCTOR USING TRANSLINEAR CONVEYORS

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An ideal floating inductor circuit using translinear conveyors is introduced. The floating inductor simulator uses two translinear conveyors and a single capacitor in its realization. The circuit provides the current controlled ideal floating inductance without any component matching constraints. Simulation results on the floating inductor simulator verify the theory.

Keywords: Active networks; Current conveyors; Active inductors

1 INTRODUCTION

Recently the translinear conveyors, i.e. current controlled conveyors (CCCIIs), have become popular because of their high performance coupled with the broad and linear electronic tunability they provide to circuit realizations. As a result, several filters, oscillators and impedance function simulators using CCCIIs have appeared in recent literature [1–6].

In this paper, a novel ideal floating inductor simulator (FIS) employing CCCIIs is presented. The circuit realizes an ideal simulator without any matching constraints. The realized circuit is studied in detail. The simulation results on the FIS exhibit a close conformity with the theory.

CIRCUIT REALIZATION

The ideal floating inductance simulator circuit is shown in Figure 1. The routine analysis yields the admittance matrix as:

$$[Y] = \frac{1}{sL_{eq}} \begin{bmatrix} +1 & -1 \\ -1 & +1 \end{bmatrix}$$
, where $L_{eq} = CR_{x1}R_{x2}$. (1)

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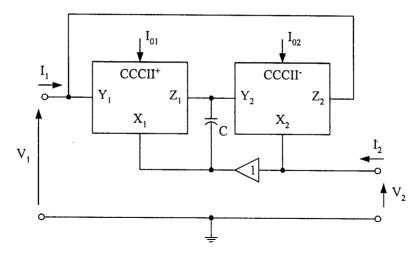


FIGURE 1 Proposed ideal floating inductance simulator (FIS).

Thus the circuit of Figure 1 realizes an ideal FIS with an inductance value L_{eq} given by (1), where R_{x1} and R_{x2} are the parasitic resistances at x-input of the respective CCCIIs. The parasitic resistance R_{xi} can be expressed as:

$$R_{xi} = \frac{V_T}{2I_{0i}} \quad i = 1, 2 \tag{2}$$

where V_T is the thermal voltage and I_{0i} (i = 1, 2) is the bias current of the *i*th CCCIIs, which is tunable over several decades [1, 2]. It is to be noted that the FIS of Figure 1 saves two CCCIIs and two external floating resistors at the cost of a voltage buffer as compared with the FIS of Ref. [7].

3 SPICE SIMULATION AND VERIFICATIONS

To validate the theory here, the FIS of Figure 1 has been used in the realization of a resonator of Figure 2. The floating inductor of Figure 2 is replaced by the FIS of Figure 1. The resonator was designed for a center frequency of $f_0 = 10 \, \text{kHz}$, and $Q_0 = 1 \, \text{with}$

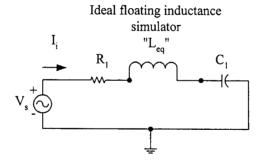


FIGURE 2 Prototype passive circuit for a resonator.

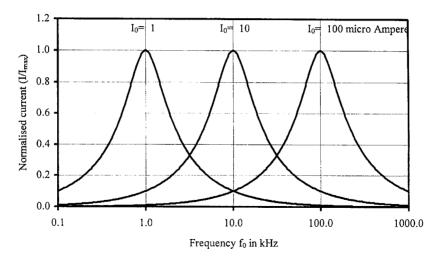


FIGURE 3 Simulated response of a FIS-based resonator for different I_0 .

 $I_{01} = I_{02} = I_0 = 10 \,\mu\text{A}$, $C = C_1 = 12.242 \,\text{nF}$ and $R_1 = 1.3 \,\text{k}\Omega$. The designed circuit was simulated using PSPICE. The CCCII⁺ and CCCII⁻ were simulated using the realizations suggested in Ref. [4]. To demonstrate the electronic tunability, the center frequency of the resonator was tuned by controlling the value of the simulated inductance through the bias current I_0 for $1 \,\mu\text{A}$, $10 \,\mu\text{A}$ and $100 \,\mu\text{A}$. The frequency response curves of normalized currents (I/I_{max}) through the resonator are shown in Figure 3, which justifies the theory.

4 CONCLUSIONS

The current controlled conveyors have been used for the realization of an ideal floating inductance simulator. In the realization only two current controlled conveyors have been used along with the single capacitor and without any component matching constraints. The simulated FIS provides wide range electronic tunability. The simulation results of the simulated floating inductance show a close conformity with the theory.

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