Dispersion-Compensating Optical Digital Filters for 40-Gb/s Metro Add–Drop Applications

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Abstract—Optical digital filters that employ optical delay lines are proposed. Properly locating the zeros in the transfer function is shown to enable interleaver pairs to be designed with the same amplitude responses but opposite phase responses. In metro add–drop applications, the interleaver pair can provide up to 50% of the adding and dropping of the total traffic while simultaneously reducing the insertion loss of the express channel. Both experiments and Monte Carlo simulations demonstrated that the cascaded dispersion of seven pairs of interleavers within the +/-30-GHz passband is less than 1 ps. A total dispersion near zero considerably increases the effective bandwidth and greatly increases the number of cascadable add–drop nodes.

Index Terms—Birefringence, metropolitan area network, optical fiber communication, optical fiber devices.

I. INTRODUCTION

E LECTRONIC digital filters, or discrete time filters [1], which employ electrical delay lines, are extensively used in digital electronic circuits. Borrowing the concept from the field of electronics, optical engineers have used optical delay lines to generate optical filter functions. Depending on whether the transfer function has poles, an optical digital filter can be classified as a finite impulse response (FIR) filter and an infinite impulse response filter [1]–[3]. FIR filters can be designed to have the same amplitude responses but have the opposite phase responses by properly locating the zeros in the transfer functions [1], [3]. Most optical digital filters include optical delay lines, directional couplers, and phase shifters. Their applications include (de)multiplexing [4]–[6], adding–dropping [7], equalizing gain [8], and compensating for dispersion [9].

Dense wavelength-division-multiplexing (DWDM) optical systems depend on good optical filters to meet the strict requirements of amplitude response; the ideal filters for such applications are perfectly rectangular filters. The phase response of such filters is equally important in attaining high bandwidth utilization; the ideal filter has a perfectly linear phase response and, therefore, a constant delay without dispersion.

Many DWDM systems use interleavers as multiplexers and demultiplexers. In a metro system, a pair of interleavers can be used in add–drop applications to provide up to 50% adding and dropping of total traffic while simultaneously reducing the

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Fig. 1. Possible configuration of an L-2L interleaver. PBS: polarization-beam splitter. YWC: YVO₄ walkoff crystal. HWP: half-wave plate.

insertion loss associated with the express channels. In such an application, the two factors that limit the maximum number of cascadable nodes are the passband (amplitude response) and the group delay (phase response). An interleaver with a wide passband and a linear phase response is preferred. As the data rate increases, the system becomes more sensitive to variations in dispersion within the signal bandwidth. Accordingly, the linear phase is a crucial parameter in defining the useable effective passband in 40-Gb/s systems.

Based on optical digital filter theory, a dispersion-compensating interleaver pair is presented. These interleavers have a 0.5-dB passband that exceeds 60 GHz and they support both positive and negative phase response.

II. USING BIREFRINGENT CRYSTAL TO DESIGN DISPERSION-COMPENSATING INTERLEAVERS

For a long time, birefringent crystal has been used in designing optical filters; such filters comprise birefringent crystal plates and polarizers. The two basic types of birefringent filters are Lyot–Öhman [10], [11] filters and Šolc [12] filters. Both are based on interference between polarized light, which depends on phase retardation between the components of light polarized parallel to the slow and the fast axes of the crystal. Consequently, birefringent crystal is used as an optical delay line, and a half-wave plate is used to alter the polarization between the delay stages. An optical FIR digital filter can, thus, be made by cascading delay lines and controlling the angle of rotation between half-wave plates. The half-wave plates can also be considered to be rotated to generate required Fourier frequency components.

Fig. 1 depicts the configuration of the interleaver that incorporated the birefringent crystal. At the input and outputs of the interleaver, YVO_4 walkoff crystal and a half-wave plate were used to ensure that light that passed through the optical delay cells was polarized in only one direction. Each delay cell includes two

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birefringent crystals, namely YVO_4 and rutile (TiO₂), to compensate for any change in temperature. The lengths of YVO_4 and rutile are determined using the following:

$$\Delta n_1 L_1 - \Delta n_2 L_2 = m \lambda_{\text{center}} = \frac{c}{\text{FSR}} \tag{1}$$

$$\frac{a}{dT}(\Delta n_1 L_1 - \Delta n_2 L_2) = 0 \Rightarrow \Delta n_1 L_1 \beta_1 - \Delta n_2 L_2 \beta_2 = 0$$
(2)

with

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$$\beta_i \equiv \frac{d\Delta n_i}{\Delta n_i dT} + \frac{dL_i}{L_i dT} = \frac{d\lambda_i}{\lambda_i dT}, \qquad i = 1, 2.$$
(3)

In (1)–(3), Δn_1 and Δn_2 represent the difference between group indexes of the ordinary and extraordinary axes of YVO₄ and rutile, respectively, c is the speed of light, FSR is the free spectral range, L_i is the length of the crystal, λ_{center} is the central wavelength in the range of operation wavelengths, m is the order of the birefringent wave plate, and β_i is the normalized variation in the wavelength with temperature. For YVO₄ and rutile, the difference between group indexes are 0.2139 and 0.2652, respectively, and the value of β are -26.54×10^{-6} 1/°C and -99.06×10^{-6} 1/°C, respectively. At the central frequency of 193.5 THz, these values of β correspond to 5.13 and 19.17 GHz/ $^{\circ}$ C, respectively. For a 100-GHz interleaver (whose FSR equals 200 GHz), solving (1) and (2) yields the lengths of the YVO₄ and rutile crystals, which are 9.5697 and 2.0685 mm, respectively. Compensation reduces the variation in the temperature of the delay cell to around 0.056 GHz/°C; thus, 99% of the variation in temperature was compensated for. For a temperature changing from 0 °C to 65 °C, the compensated delay cell has a central frequency drift of around 3.7 GHz.

The perfect interleaver function is a periodic square-wave response in the frequency domain. A square-wave amplitude response has only odd Fourier frequency components. Equations (4) and (5) state the Fourier frequency components generated by L-2L and L-3L delay lines, when the FSR of the unit delay cell L equals ω . For a digital filter with only odd Fourier frequency components, the L-2L, and not the L-3L, configuration must be selected

$$L - 2L : \begin{bmatrix} \frac{\omega}{2} \\ -\frac{\omega}{2} \end{bmatrix} \times \begin{bmatrix} \omega \\ -\omega \end{bmatrix} \Rightarrow \begin{bmatrix} \frac{3\omega}{2} \\ \frac{\omega}{2} \\ -\frac{3\omega}{2} \end{bmatrix} \xrightarrow{\text{amplitude}} \begin{bmatrix} 3\omega \\ \omega \\ -\omega \\ -3\omega \end{bmatrix} \quad (4)$$
$$L - 3L : \begin{bmatrix} \frac{\omega}{2} \\ -\frac{\omega}{2} \end{bmatrix} \times \begin{bmatrix} \frac{3\omega}{2} \\ -\frac{3\omega}{2} \end{bmatrix} \Rightarrow \begin{bmatrix} 2\omega \\ \omega \\ -2\omega \\ -\omega \end{bmatrix} \xrightarrow{\text{amplitude}} \begin{bmatrix} 4\omega \\ 2\omega \\ -2\omega \\ -4\omega \end{bmatrix} . \quad (5)$$

Higher-frequency Fourier components must be included to sharpen the frequency edge response. More delay line stages must be added to increase the highest Fourier frequency; the passband, the insertion loss, the size, the reliability, and the cost are all traded off against each other. For a 100-GHz interleaver, L-2L-2L can be used to meet the passband and delay requirements for 40-Gb/s transmission without difficulty. If only a 10-Gb/s signal is transmitted, an L-2L design will suffice. Once



Fig. 2. (a) Simulated amplitude and delay responses of two types of interleavers. (b) Simulated cascaded amplitude and delay response of a dispersion-compensating pair of interleavers.

the configuration has been selected, the angles of the wave plates must be determined. Optimization tools in standard Matlab or Mathematica programs can be used. The goal is to find the wave plate angles that will minimize

$$\min\left\{\sum_{n=1}^{N} \left[\hat{x}(f_n) - x(f_n)\right]^2\right\}$$
(6)

with

$$f_c - \frac{\text{FSR}}{2} \le f_n \le f_c + \frac{\text{FSR}}{2}$$

In (6), $\hat{x}(f_n)$ is the target or ideal interleaver transmission function and $x(f_n)$ is the real transmission function. The transmission function is periodic, so the errors are only summed over one FSR at the central frequency, f_c . The input polarization angle was shifted by 90° by changing the position of the half-wave plate at the input walkoff crystal to generate two delay responses.

Fig. 2(a) presents the simulated amplitude and delay response of two types of L-2L-2L interleavers. As depicted, changing the input polarization yields two interleavers that have the same amplitude responses, but opposite delay responses. Fig. 2(b) shows the cascaded total amplitude and delay response. As illustrated in this figure, the cascaded interleaver pair will have constant delay and, therefore, a dispersion that is close to zero.

Fig. 3 plots the measured amplitude responses and the delay responses of two types of 100-GHz L-2L-2L interleavers. Fig. 3(a) presents the folded transmission responses in a frequency range from 191.8 to 195.5 THz. The width of the



Fig. 3. (a), (b) Measured folded amplitude responses of multiplexer (Mux) and demultiplexer (Demux). (c), (d) Measured folded delay responses of Mux and Demux.

0.5-dB passband is 60.8 GHz and the insertion loss is 1.45 dB over all polarizations and temperatures (from 0 °C to 65 °C). As illustrated in Fig. 3(a)–(d), Type A and B interleavers have identical transmission spectra but reversed delay responses. Type A and B interleavers can be cascaded to generate a linear phase interleaver pair with a total dispersion near zero, which characteristic is desirable, particularly in metro add–drop applications and/or high bit rate transmission systems.

A Monte Carlo simulation of manufacturing imprecision was conducted to estimate the design tolerance and the cascaded delay performance. Two parameters were found to dominate the performance-the lengths of the crystals and the angles of the half-wave plates. The variation of the lengths of crystals and the angles of the half-wave plates were assumed to follow a Gaussian distribution with standard deviations σ of 0.5 μ m and 0.6°, respectively. Fig. 4(a) shows the simulation results obtained for ten cascaded interleaver pairs; the total delay is under 0.5 ps within the passband +/-30 GHz. Fig. 4(b) plots the measured results made of seven cascaded interleaver pairs. The total delay is under 1 ps in the passband. Although the recirculating loop simulation and the experiment to estimate the impact on system performance have not been completed, the authors are optimistic that the dispersion will minimally degrade a 40-Gb/s system.

III. CONCLUSION

Dispersion-compensating interleaver pairs for metro add-drop applications have been designed and experimentally demonstrated. Seven pairs of cascaded Type A or B interleavers have a 0.5-dB passband width that is wider than 60 GHz, insertion losses of less than 1.5 dB, and maximum group delays of under 1 ps. A total dispersion of around zero considerably increases the effective bandwidth and greatly increases the number of cascadable add-drop nodes.



Fig. 4. (a) Simulated cascaded delay responses and (b) measured cascaded delay responses.

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