

On Necessary Padding with IO

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Abstract

We show that the common proof technique of padding a circuit before IO obfuscation is sometimes necessary. That is, assuming indistinguishability obfuscation (IO) and one-way functions exist, we define samplers Sam_0 , which outputs (aux_0, C_0) , and Sam_1 , which outputs (aux_1, C_1) such that:

- The distributions $(\text{aux}_0, \text{IO}(C_0))$ and $(\text{aux}_1, \text{IO}(C_1))$ are perfectly distinguishable.
- For padding $s = \text{poly}(\lambda)$, the distributions $(\text{aux}_0, \text{IO}(C_0 \| 0^s))$ and $(\text{aux}_1, \text{IO}(C_1 \| 0^s))$ are computationally indistinguishable.

We note this refutes the recent “Superfluous Padding Assumption” of Brzuska and Mittelbach[BM15].

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1 Introduction

Proofs of security for cryptographic constructions using indistinguishability obfuscation (IO) typically show that the obfuscations of two circuits are indistinguishable if those circuits are artificially padded to a larger size. This is a consequence of the fact that IO guarantees indistinguishability of two obfuscated circuits only if the input circuits are of equivalent sizes. But because of the ubiquitous hybrid argument style of proof, the two circuits must typically be padded not only to the size of larger circuit, but rather to the size of the largest circuit in the hybrid argument.

At this point, one may wonder whether this padding is an artifact of our security proof, or whether it is really necessary. In this work, we show that in the presence of arbitrary auxiliary information, there are distributions of equally-sized pairs of circuits, whose obfuscations are indistinguishable only if sufficiently padded.

1.1 Preliminaries

We assume familiarity with puncturable pseudorandom functions [BW13, BGI14]. In particular, we will use the fact that if one-way functions exist, then there is a puncturable PRF family $\mathcal{F} = \{\mathcal{F}_\lambda\}_{\lambda > 0}$ in which each $f \in \mathcal{F}_\lambda$ maps $\{0, 1\}^\lambda$ to $\{0, 1\}$.

We also assume the existence of an indistinguishability obfuscator [GGH⁺13]. This is a p.p.t. Turing machine iO such that:

- $\text{iO}(C, 1^\lambda)$ outputs a circuit which is functionally equivalent to C .
- If C and C' are two circuits of the same size and same functionality, then the advantage of any p.p.t. adversary in distinguishing $\text{iO}(C, 1^\lambda)$ from $\text{iO}(C', 1^\lambda)$ is negligible in λ .

We will frequently omit the security parameter 1^λ as an argument of iO .

We will write $C \parallel 0^s$ to denote a *padded* version of the circuit C , which is of size $|C| + s$.

1.2 Techniques

We want to show a pair of distributions (aux_0, C_0) and (aux_1, C_1) such that:

- $(\text{aux}_0, \text{iO}(C_0))$ is distinguishable from $(\text{aux}_1, \text{iO}(C_1))$
- For some padding p , $(\text{aux}_0, \text{iO}(C_0 \parallel 0^p))$ is indistinguishable from $(\text{aux}_1, \text{iO}(C_1 \parallel 0^p))$.

In our construction, C_0 and C_1 are defined simply as circuits which evaluate a (puncturable) PRF. aux_0 and aux_1 are (sufficiently padded) obfuscated circuits, each of which have this same PRF inside. aux_b takes a “small” circuit as input, and checks whether this circuit agrees with the PRF on a “large” set of test inputs. If it does, then aux_b outputs b . Otherwise, aux_b outputs 0.

“Small” and “large” are chosen so that the obfuscated C_0 or C_1 is small, but not large.

The first bullet is easy; the slightly tricky part is to show that when C_0 and C_1 are padded to be large (even before obfuscation is applied), then $(\mathbf{aux}_0, \mathcal{IO}(C_0))$ is indistinguishable from $(\mathbf{aux}_1, \mathcal{IO}(C_1))$. We show this in a three-step hybrid argument, starting with $(\mathbf{aux}_b, \mathcal{IO}(C_b))$ for arbitrary b .

1. The PRF in $\mathbf{aux}_b$ and in C_b is punctured on the whole set of test inputs, and the corresponding test values are hard-coded. This is indistinguishable by $\mathcal{IO}$.
2. These test values are replaced by truly random bits. This is indistinguishable by the puncturable PRF security.
3. Now there statistically does not exist any small circuit which agrees with all the test values. So $\mathbf{aux}_b$ is replaced by the obfuscated all zero function, which is indistinguishable by $\mathcal{IO}$.

This last hybrid distribution on $(\mathbf{aux}_b, \mathcal{IO}(C_b))$ is independent of the bit b .

1.3 Related Work

We note a similarity between our work and the previous work of Goldwasser and Kalai [GK05], which amongst other things shows the impossibility of VBB-obfuscating pseudorandom functions given auxiliary information. They use the fact that oracle access to a pseudo-entropic circuit C , does not reveal how to find a small circuit that agrees with C . In particular, suppose one is given an obfuscated circuit which tests whether a (small) input circuit agrees with C , and if so outputs a secret bit b . Now one can compute the bit b given any obfuscation of C , but not given black-box access to C .

Our result modifies this argument to assume only that the underlying obfuscators are indistinguishability obfuscators instead of VBB obfuscators. We show that one can compute the bit b given any sufficiently small obfuscation of C (i.e. $\mathcal{IO}(C)$), but not given an obfuscation of a functionally equivalent C' which has been padded to be larger (i.e. $\mathcal{IO}(C \parallel 0^s)$).

In personal communication, Nir Bitansky points out that the results of [BCC⁺14] may yield an alternative proof of our result, and that ordinary PRFs (not necessarily puncturable) and witness encryption (which is implied by IO) suffice to prove our main result.

2 Main Result

We now prove our main result more formally. Let q be a polynomial such that $q(\lambda)$ bounds the size of $\mathcal{IO}(f, 1^\lambda)$, when f is sampled from the punctural PRF family $\mathcal{F}_\lambda$.

We now define a pair of algorithms $(\mathbf{Sam}_0, \mathbf{Sam}_1)$. $\mathbf{Sam}_b$ will output a pair $(\mathbf{aux}_b, C_b)$. The algorithm $\mathbf{Sam}_b(1^\lambda)$ first samples a puncturable PRF $f \leftarrow \mathcal{F}_\lambda$.

Sam_b first computes $\text{aux}_b \leftarrow \text{iO}(A_{b,f})$, where $A_{b,f}$ is described in Algorithm 1, and Sam_b then computes $C_b = B_f$, where B_f is described in Algorithm 2.

Input: Circuit C of size $q(\lambda)$
Data: Bit b , PPRF f
1 **if** $C(i) = f(i)$ for all $i \in \{0, \dots, q(\lambda) + \lambda\}$ **then**
2 **return** b
3 **else**
4 **return** 0
5 **end**

Algorithm 1: Circuit $A_{b,f}$

Input: $x \in \{0, 1\}^\lambda$
Data: PPRF f
1 **return** $f(x)$.

Algorithm 2: Circuit B_f

Claim 1. *The distributions $(\text{aux}_0, \text{iO}(C_0))$ and $(\text{aux}_1, \text{iO}(C_1))$ are perfectly distinguishable when $(\text{aux}_b, C_b) \leftarrow \text{Sam}_b(1^\lambda)$.*

Proof. aux_b computes the same function as $A_{b,f}$ and C_b is B_f . The claim follows simply because $A_{b,f}(\text{iO}(B_f)) = A_{b,f}(B_f) = b$. $\square$

Claim 2. *For some padding p_b , the distributions $(\text{aux}_0, \text{iO}(C_0 \| 0^{p_b}))$ and $(\text{aux}_1, \text{iO}(C_1 \| 0^{p_b}))$ are computationally indistinguishable when $(\text{aux}_b, C_b) \leftarrow \text{Sam}_b(1^\lambda)$.*

Proof. Let p_b be padding so that $|B_f \| 0^{p_b}| = |B_f^1|$, where B_f^1 is described in Algorithm 4. We define three indistinguishable hybrid distributions H_b^1 through H_b^3 such that:

- H_b^1 is indistinguishable from $(\text{aux}_b, \text{iO}(C_b \| 0^{p_b}))$ when $(\text{aux}_b, C_b) \leftarrow \text{Sam}_b(1^\lambda)$.
- H_b^3 is independent of b .

Hybrid H_b^1 : Hybrid H_b^1 is sampled by first sampling a PPRF $f \leftarrow \mathcal{F}_\lambda$, and puncturing it on the set $\{0, \dots, q(\lambda) + \lambda\}$ to obtain the punctured PRF f' . Let $y_i = f(i)$ for $i \in \{0, \dots, q(\lambda) + \lambda\}$.

H_b^1 then consists of $(\text{iO}(A_{b,f}^1), \text{iO}(B_f^1))$. The circuit $A_{b,f}^1$ is described in Algorithm 3, with the values y_i hard-coded, and is padded to be as large as $A_{b,f}$. The circuit B_f^1 is described in Algorithm 4, with the PPRF f' and the values y_i hard-coded.

Hybrid H_b^2 : Hybrid H_b^2 is sampled identically to H_b^1 , but each y_i is sampled uniformly at random.

Input: Circuit C of size $q(\lambda)$ Data: Bit b, values y_i <pre> 1 if $C(i) = y_i$ for all $i \in \{0, \dots, q(\lambda) + \lambda\}$ then 2 return b 3 else 4 return 0 5 end </pre>

Algorithm 3: Circuit $A_{b,f}^1$

Input: $x \in \{0, 1\}^\lambda$ Data: Punctured PPRF f', values y_i for $i \in \{0, \dots, q(\lambda) + \lambda\}$ <pre> 1 if $x \in \{0, \dots, q(\lambda) + \lambda\}$ then 2 return y_x; 3 else 4 Return $f'(x)$; 5 end </pre>
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Algorithm 4: Circuit B_f^1

Hybrid H_b^3 : In hybrid H_b^3 , the circuit $A_{b,f}^1$ is replaced with the constant zero function, appropriately padded.

Claim 3. $H_b^1 \approx \text{Sam}_b(1^\lambda)$.

Proof. This follows from the security of iO : the obfuscated circuits have the same functionality and size in both H_b^1 and $\text{Sam}_b(1^\lambda)$. $\square$

Claim 4. *Hybrid $H_b^2 \approx H_b^1$.*

Proof. This follows from the pseudorandomness of the punctured PRF f' at the (selectively) punctured set $\{0, \dots, q(\lambda) + \lambda\}$. $\square$

Claim 5. *Hybrid $H_b^3 \approx H_b^2$.*

Proof. This follows from the security of iO . A simple counting argument implies that with high probability (at least $1 - 2^{-\lambda}$), there is no circuit C of size $q(\lambda)$ such that $C(i) = y_i$ for all $i \in \{0, \dots, q(\lambda) + \lambda\}$. Thus the circuit $A_{b,f}^1$ with truly random y_i 's is functionally equivalent to the constant zero circuit with high probability. $\square$

This completes the proof of Claim 2. $\square$

3 Extensions

3.1 Variants of the Superfluous Padding Assumption

One possible restriction on Sam_0 and Sam_1 , proposed by [BM15] as a weaker assumption, requires the marginal distribution of aux_0 to be the same as the marginal distribution of aux_1 . While this does not hold for our counterexample, it can be easily modified to have this property. Rather than having aux_b output the bit b , aux_b outputs a random string r . On input r , C_b outputs b .

The proof techniques above, when applied to this modified construction, show how to move to a hybrid where aux_b is independent of r . We can then apply a standard injective PRG trick to make C_b independent of r and of b .

3.2 Implication About Double Obfuscation

The necessity of superfluous padding implies a surprising result. If iO increases the size of circuits, then there are efficiently sampleable distributions on (aux_0, C_0) and (aux_1, C_1) such that $(\text{aux}_0, \text{iO}^k(C_0))$ is indistinguishable from $(\text{aux}_1, \text{iO}^k(C_1))$ for some integer $k > 1$, but $(\text{aux}_0, \text{iO}(C_0))$ is perfectly distinguishable from $(\text{aux}_1, \text{iO}(C_1))$. This follows from our construction of Sam_0 and Sam_1 because the inner $k - 1$ obfuscations are functionally equivalent to padding.

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