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Low-Complexity Two-Stage Carrier Phase Estimation for 16-QAM Systems using QPSK Partitioning and Maximum Likelihood Detection

Yuliang Gao^{1, 3}, Alan Pak Tao Lau¹, Chao Lu², Jian Wu³, Yan Li³, Kun Xu³, Wei Li³ and Jintong Lin³ ¹Photonics Research Center, Department of Electrical Engineering, The Hong Kong Polytechnic University, Hung Hom, Kowloon, Hong Kong

otonics Research Center, Department of Electrical Engineering, The Hong Kong Polytechnic University, Hung Hom, Kowloon, Hong Kon ²Photonics Research Center, Department of Electronic and Information Engineering, The Hong Kong Polytechnic University, Hung Hom, Kowloon, Hong Kong ³Key Laboratory of Information Photonics and Optical communications, Ministry of Education in China, Beijing University of Posts and Telecommunications, Beijing, China, 100876.

E-mail: <u>eeylgao@polyu.edu.hk</u>

Abstract: A feedforward carrier phase estimation algorithm using a modified QPSK partition approach followed by maximum-likelihood (ML) detection is proposed. Results demonstrate complexity reduction by more than a factor of 2 compared with other techniques. **OCIS codes:** (060.4510) Optical communications; (060.1660) Coherent communication; (060.5060) Phase modulation

1. Introduction

Quadrature Amplitude Modulation (QAM) combined with digital signal processing with its higher spectral efficiency has become a promising candidate for future optical transmission networks. However, performance requirements for carrier phase noise compensation become very stringent for QAM signals. Thanks to fast digital signal processors (DSPs), frequency offset and carrier phase noise can be recovered using digital signal processing at the receiver, thus allowing for the use of free running local oscillators (LO).

As real-time realization of DSP for coherent systems has become a central topic, complexity is one of the major concerns for research in advanced DSP algorithms [1]. Various feedforward carrier recovery algorithms for 16-QAM systems have been proposed, such as QPSK partitioning and blind phase search (BPS) techniques [2-4]. For QPSK partition, a linewidth times symbol duration product of 1×10^{-4} is achievable for a bit error rate (BER) of 1×10^{-3} , but this method relies on the use of pilot sequence to eliminate the phase ambiguity and cycle slip problems. On the other hand, BPS demonstrates the best phase noise tolerance according to our knowledge but comes with an expense of high complexity [3]. Although this problem has been relieved by reducing the number of test phases and using subsequent maximum-likelihood detection (BPS/ML) [4], the computation effort still remains relatively high.

In this paper, a low-complexity, feedforward and two-stage carrier phase estimation (CPE) algorithm for 16-QAM is proposed using a modified QPSK partition scheme followed by maximum-likelihood detections (QPSK partition/ML). The tolerance of linewidth and complexity of various CPE algorithms are investigated and compared with each other. We show that the proposed technique can reduce the complexity by more than a factor of 2 without any performance degradation.

2. Low-complexity two-stage carrier phase estimator for 16-QAM systems

Consider a 16-QAM system where the received signal is sampled and processed in a DSP. After CD and possibly nonlinearity compensation, timing recovery, polarization demultiplexing, and resampling to 1 sample/symbol, the n^{th} received symbol can be expressed as:

$$s_n = b_n \exp[j(\theta_n + 2\pi\Delta f nT_s)] + z_n \tag{1}$$

where $b_n \in \{\pm 1 \pm j, \pm 3 \pm 3j, \pm 1 \pm 3j, \pm 3 \pm j\}$ are the 16-QAM symbols, θ_n is the combined phase noise of the transmitter laser and LO, T_s is the symbol duration and z_n is amplified spontaneous emission (ASE) noise generated from inline amplifiers. The phase difference between two adjacent symbols ($\theta_{n+1}-\theta_n$) can be modeled as zero-mean Gaussian random variable with variance $\sigma^2 = 2\pi \Delta v \cdot T_s$ where Δv is the combined linewidth of the transmitter lasers and LO.

The partitioning of square 16-QAM symbols for the QPSK partition scheme is shown in Fig. 1 where symbols are classified into three rings Class I (C_1), Class II (C_2) and Class III (C_3) according to their amplitudes. In the inner and outer rings, C_1 and C_3 symbols are two QPSK constellation sets with different optical signal-to-noise ratios (OSNRs). Their modulated phase can be eliminated by Viterbi and Viterbi phase estimation (VVPE) [5]. The block diagram of the proposed algorithm is shown in Fig. 2 (a).

n .



Fig. 1. Partition for 16-QAM signals

Fig. 2. (a)Block diagram of the proposed algorithm; (b) Modified VVPE; (c) ML Estimator.

The input symbols are first partitioned and normalized according to their amplitudes. If s_n belongs to Class I symbols (C_1) or Class III symbols (C_3), they are first selected to be processed by a modified VVPE [6] to obtain the first phase estimate θ_n^{est1} . The carrier phase estimate can be expressed as

$$\theta_n^{est1} = \frac{1}{4} \cdot \left\{ \sum_{i:s_i = C_1} \frac{s_i^4}{|s_i^4|} + W_1 \cdot \sum_{i:s_i = C_3} \frac{s_i^4}{|s_i^4|} \right\}$$
(2)

where W_1 is a weighting coefficient which takes into account the fact that symbols in C_3 have a higher OSNR and hence carrier phase estimates from these symbols should be better than those from C_1 . Afterwards, the symbols in C_2 are first compensated with the estimated phase θ_n^{est1} and the compensated symbols together with their decisions are fed into an maximum likelihood phase estimator, to obtain a residue phase error θ_n^{est2} . This is then combined with θ_n^{est1} to obtain a more accurate phase noise of the first stage $\theta_n^{est1} = \theta_n^{est1} + W_2 \cdot \theta_n^{est2}$, where W_2 is another weight coefficient related to the proportion of symbols in the 2N block that belong to C_2 and relative OSNRs among different classes of symbols. To obtain further accuracies, the recovered symbols at the output of the first stage x_n are again fed into a second stage ML estimator shown in Fig. 2 (c). The ML estimation of the carrier phase θ^{ML} can be described as

$$h_{n} = \sum_{k=n-M+1}^{n+M} x_{k} \cdot \hat{y}_{k}^{*}$$
(3)

$$\theta_n^{ML} = \tan^{-1}(\operatorname{Im}[h_n]/\operatorname{Re}[h_n])$$
(4)

where \hat{y}_k is the decision of x_k and 2M is the block length used in the stage II ML estimator.

3. Simulation results and discussion

Simulation results for 16-QAM signals with different linewidths and OSNRs using QPSK Partition, BPS, BPS/ML and the proposed technique are shown in Fig. 3 and 4. To make a fair comparison, we modified the QPSK partition method to be differential encoded, thus avoiding the need of pilot symbols. The weights W_1 , W_2 and filter widths are optimized numerically for the proposed QPSK Partition/ML and improves the $\Delta v T_s$ tolerance by about 1dB to 1×10^{-4} for a BER of 1E-3. Also, QPSK partition/ML has the best performance among all techniques for $\Delta v T_s$ larger than 2×10⁻⁴. The performance of QPSK Partitioning [2] is noticeably worse than others. Fig. 4 shows the empirical distribution of the residual phase errors using the proposed technique, BPS/ML and BPS with $\Delta v T_s = 5 \times 10^{-4}$. It can be seen that when the $\Delta v T_s$ is as large as 5×10^{-4} , variance of phase error for QPSK partition/ML, BPS/ML and BPS are 3.99×10^{-3} rad², 5.56×10^{-3} rad² and 5.93×10^{-3} rad² respectively, thus indicating the improved performance of the proposed technique.

The required processing complexity for the proposed QPSK Partition/ML scheme can be derived from Fig. 2. Here, we discuss the required operations for 2N symbols. In the partition and normalization block, 2N look-up tables, 4N complex multipliers together with 2N selectors are required. In the following modified VVPE, the N symbols in C_1 or C_3 (on average) require 2N complex multipliers, N-1 complex adders, 1 real multipliers and 1 look-up table to compute θ_n^{est1} . While in the first ML estimator, N complex multipliers, N-1 complex adders, N decision blocks, and

1 look-up table are needed. Since the second ML estimator is supposed to process twice as many symbols in the first one, its complexity is also doubled. Beside these four main blocks there are several simple operations: 3 compensation rotations (5N complex multipliers and 3 look-up tables), 2 weighting operations (only 2 real multipliers) and 1 unwrapping (1 comparator and 1 real adder).



Fig. 4. Empirical distribution of the residual error of CPE algorithms $\Delta v \cdot T_s$ is 5×10^{-4} while the OSNR is 20dB

Table 1. shows the complexity comparisons of the various feedforward algorithms studied in this paper. When discussing the complexity in comparison with the BPS/ML and BPS schemes, we assume 14 and 32 test phases respectively for comparable performance for 1dB sensitivity penalty. From Table.1, it can be seen that for various types of operations, the proposed algorithm requires a complexity that is less than one half and one fourth of BPS/ML and BPS CPE techniques respectively. Although the QPSK partition method is slightly simpler, its performance is noticeably worse than the proposed technique and others studied.

Methods	complex multipliers	real multipliers	decisions	real adders	selectors	look-up table	comparators
QPSK Partion	14N	1	2 <i>N</i>	4 <i>N</i> -2	3N	2 <i>N</i> +2	N+1
QPSK Partition/ML	14N	2	5N	8 <i>N</i> -7	2 <i>N</i>	2 <i>N</i> +6	1
BPS/ML	32N	-	30N	32 <i>N</i> +12	28N	29N	14
BPS	64 <i>N</i>	-	64N	64N	64N	64N	32

Table 1. Complexity comparison of various carrier recovery techniques for 16-QAM systems

4. Conclusions

A low-complexity feedforward carrier phase estimation algorithm for intradyne coherent systems using modified QPSK partition and ML detection is proposed. Results show that for the same linewidth tolerance, the computational complexity of the proposed technique is at least a factor of 2 lower than others reported in the literature and hence favors future real-time implementation of advanced feedforward carrier phase estimation techniques. In addition, our proposed technique is more tolerant towards phase noise when symbol duration times linewidth is larger than 2×10^{-4} .

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